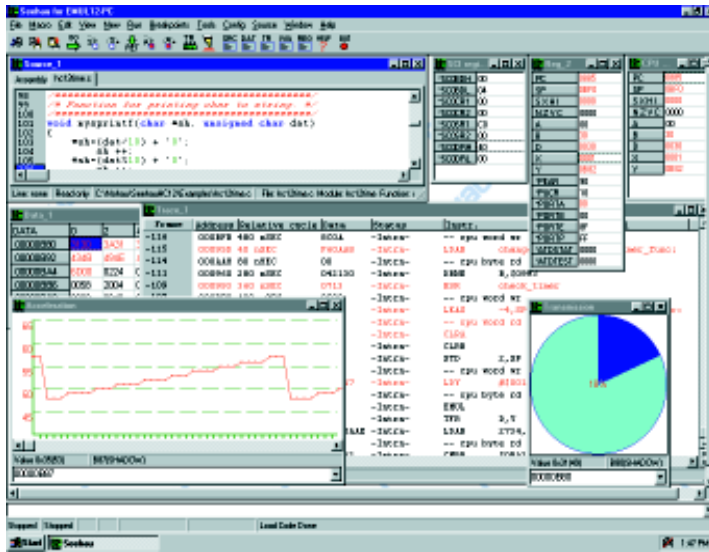


EMUL12-PC In-Circuit Emulator for the Motorola HC12 Series



Key Benefits

- Motorola 68HC12 devices supported with 2 emulator bodies.
- Full feature ICE. To 30 MHz.
- Seehau GUI: Windows 95, 98 and NT.
- Compact hand held design goes anywhere. Uses LPT port.
- Optional 128K trace board. Pipeline is fully decoded.
- All reconstructed ports provide CMOS voltage levels.
- Trace and Triggers are configured in real-time.
- No-skid hardware and software breakpoints.
- Programs FLASH and EEPROM.
- Supports all chip RESET modes.

Product Overview

EMUL12-PC supports the 68HC912B32, BC32 and D60, DA128 and DG128 (4Q99) family of microcontrollers. The EMUL12-PC is a full feature emulator. The emulator consists of an emulation board, a trace board and a controller personality module. Change only the personality module to suit your target. The trace is optional and can be added later. Seehau, the new user interface, is standard. Debugging time is reduced with advanced features.

Trace Memory and Triggers

Trace memory and the triggers are configurable and viewable without stealing CPU cycles. Full pipeline decoding ensures only executed instructions and data read/writes are captured and no false triggering occurs. The trace contents can be saved to a file. The types of cycles recorded is user defined and includes free cycles.

Triggers can be set on addresses and data ranges including addresses internal to the target chip. Triggers control trace recording and can cause the emulator to stop the target depending on the options set. Trace and Triggering can record all internal and external accesses in all cases: and always in real-time.

Shadow RAM

Shadow RAM allows data writes in real-time to be displayed in the Data window. The data can be displayed in many numerical and graphical formats. User memory reads and writes to all memory space are possible in real-time via the BDM interface.

Modes Supported

The EMUL12-PC supports all operating modes of the HC12. Single-Chip, Expanded Wide (16 bit) and Expanded Narrow (8 bit) modes are available with full access to the chip resources. These modes can be switched on-the-fly. All reset modes of the HC12 are supported.

Port Replacement Unit (PRU)

The PRU provides CMOS levels rather than TTL to the outside world. This replicates the HC12 more accurately. The address and data bus is always presented allowing advanced emulator features such as no skid hardware breakpoints. Vdd is 2.7 to 5.25 volts.

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