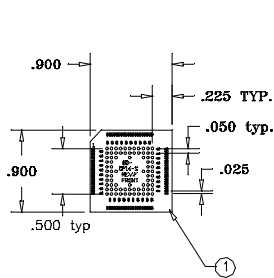
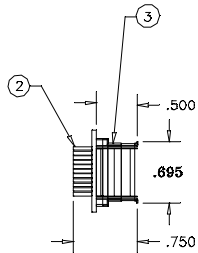


REV. F2 F1199

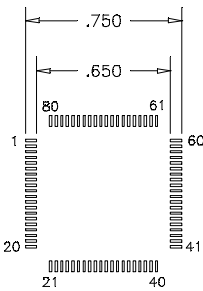


PIN MAP

1	80	78	76	74	72	69	67	65	63	61
3	2	79	77	75	73	70	68	66	62	60
5	6	4	71					64	59	58
7	8								57	56
9	10								55	54
12	13	11						51	53	52
14	15								50	49
16	17								48	47
18	19	24	31					44	46	45
20	22	26	28	30	33	35	37	39	42	43
21	23	25	27	29	32	34	36	38	40	41



RECOMMENDED MINIMUM PAD LAYOUT



	Emulation Technology, Inc. — VLSI and SMT ADAPTERS and ACCESSORIES —	
	2344 Walsh Avenue, Bldg. F Santa Clara, Ca 95051	TEL: (408) 982-0660 FAX: (408) 982-0664
SHEET: 1 OF 1 CHECKED: GABOR KARDOZ DO NOT SCALE DRAWING	DATE: 7-7-95 DRAWN: PAULA MCKENNA	REVISION: ITEM: EPP080QF14SM DESCRIPTION: EPP-080-QF14-SM

NO.	ITEM	DESCRIPTION	QTY
1	80-QF14S	BASE BOARD	1
2	PINS2	MOUNT ON FRONT OF #1	80
3	QF14-SPACER-A	MOUNT ON REAR OF #1	1